FUJITSU

shaping tomorrow with you

Towards Exascale

The Fujitsu way today and tomorrow to build one the most powerful machines.

Fujitsu HPC platform solutions

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Petascale supercomputer

Fujitsu developed SPARC chips and Tofu interconnect for high performance, high reliability, and high operability

K computer
Developed with RIKEN

PRIMEHPC FX10

x86 Clusters

PRIMERGY supports latest x86 CPU & MIC and GPGPU etc. and adopts Fujitsu's latest packaging technologies for high performance and high operability

PRIMERGY CX400

BX900/BX400 RX200/RX900

XEON-PHI

Feasibility study toward Exascale

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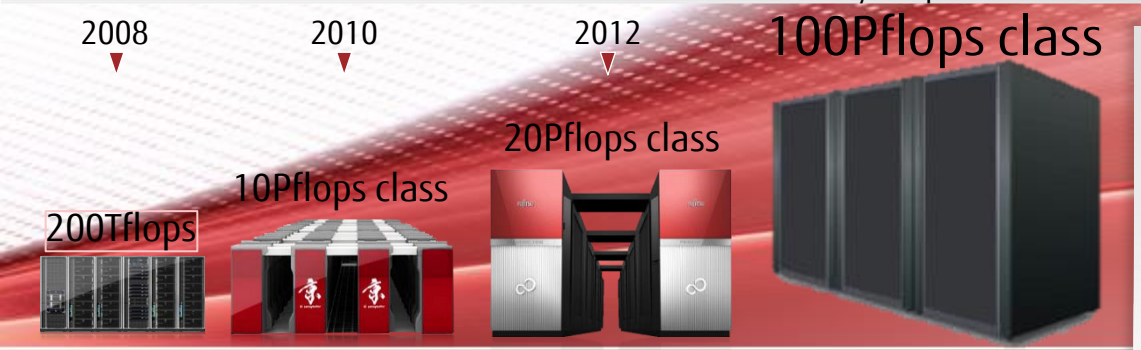
This study is a part of the "Feasibility Study on Future HPC R&D" program led by MEXT, Japan.



FX1 | K computer | PRIMEHPC FX10 | Post-FX10

- Evolution of the K computer architecture
- Co-design with various target applications
- Novel system software stack covers x86 clusters and post-petascale machines

Target Applications selected in FY2012

ALPS

Algorithms and Libraries for Physics Simulations

RSDFT

Real-Space Density-Functional Theory

COCO

CCSR Ocean Component Model

NICAM

Nonhydrostatic Icosahedral Atmospheric Model

Thank you for your attention.