

Energy-efficient High Performance Computing with SuperMUC



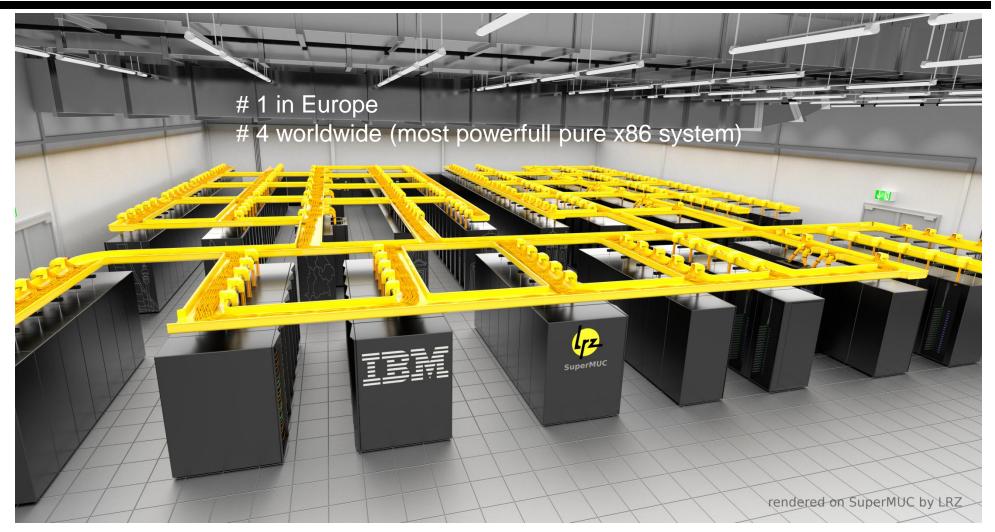


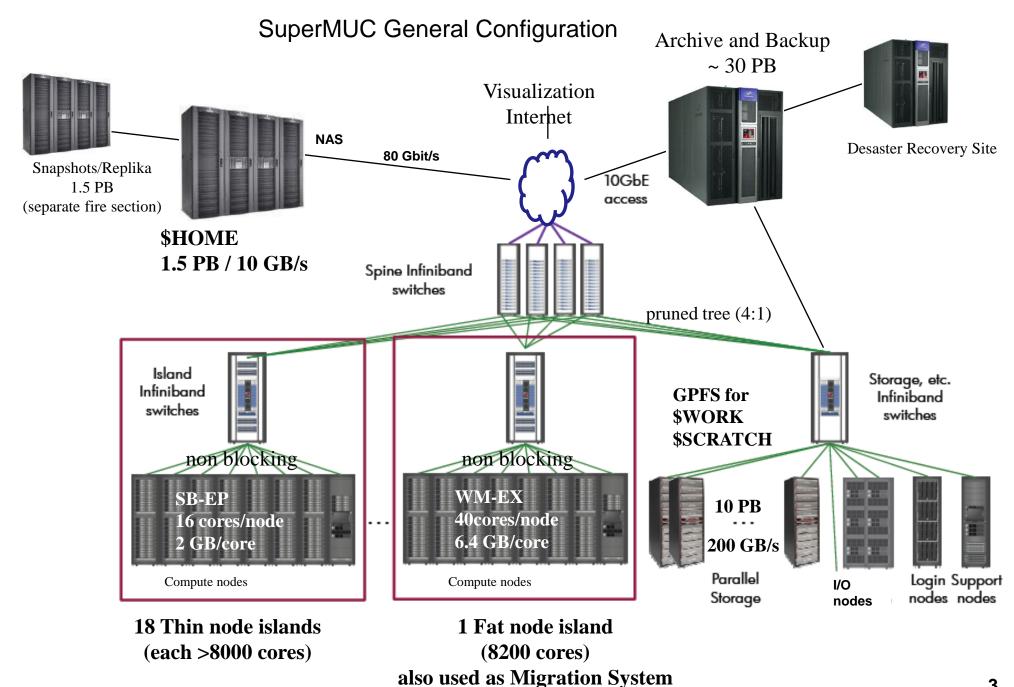
By Ernst A. Graf

Arndt Bode Chairman of the Board of the Leibniz Supercomputing Centre and Technische Universität München



SuperMUC, TOP 500 39th Release, Hamburg June 2012





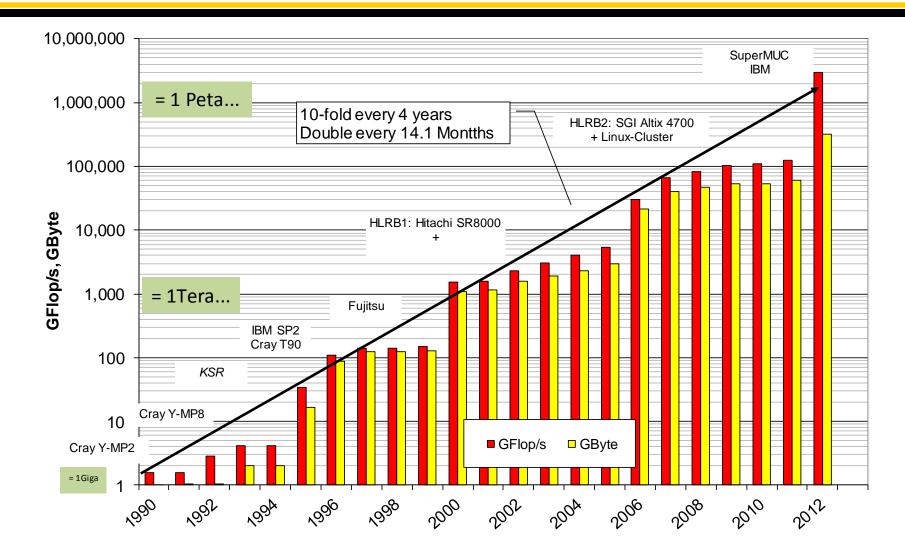
What's special about SuperMUC



2222288° SuperMUC is the most powerfull pure x86 ISA system of the world General purpose, standard programming interface, "easy" to port to, future-safe for many applications SuperMUC is the most energy efficient x86 based supercomputer of the world Dark Center infrastructure at LRZ Warm Water directly cooling Energy aware scheduling with xCAT and Load Leveler Contract including energy for 5 years



Supercomputer Architectures at LRZ since 1990





Energy Efficiency and SuperMUC

Motivation: we pay 15.8 Cents per Kwh (regular contract, expect increase)



Energy Efficient HPC



Reduce the power losses in the power supply chain

- Exploit your possibilities for using compressorless cooling und use energy-efficient cooling technologies (e.g. direct liquid cooling)
- Re-use waste heat of IT systems

- Use newest semiconductor technology
- Use of energy saving processor and memory technologies
- Consider using special hardware or accelerators tailored for solving specific scientific problems or numerical algorithms
- Monitor the energy consumption of the compute systems and the cooling infrastructure
- Use energy aware system software to exploit the energy saving features of your target platform
- Monitor and optimize the performance of your scientific applications

- Use most efficient algorithms
- Use best libraries
- Use most efficient programming paradigm

Energy efficient hardware

Energy efficient infrastructure

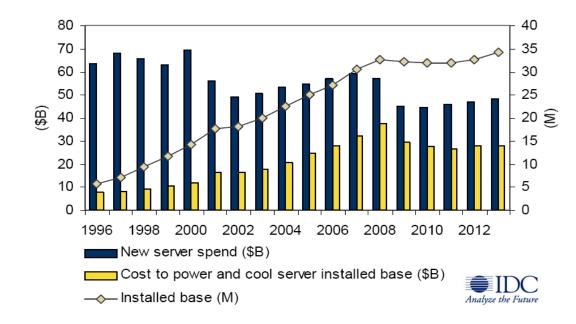
Energy aware software environment

Energy efficient applications

Data Center Market Drivers and Trends

Total cost of ownership and environmental footprint

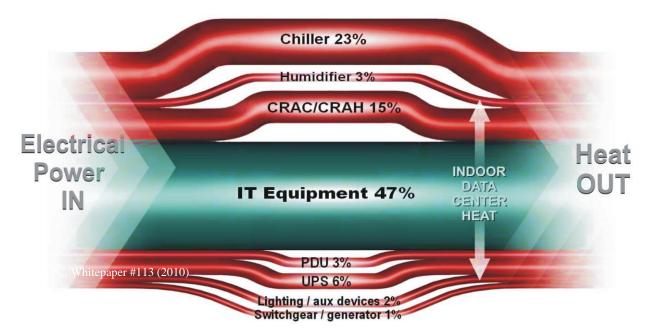
Servers used 330 TWh of electrical energy: \$25bn or 2% of the electricity production (2009).





Energy Consumption of Datacenter





□ Air-cooled datacenters are inefficient.

Typical cooling needs as much energy as IT equipment and both are thrown-away.

Provocative: datacenter is a huge "heater with integrated logic."



Water

1. High heat capacity

 $c_{_V} \approx ~1~Wh/(L{\cdot}K)$

2. Low thermal resistance



 $\dot{q}'' = R_{th} \cdot \Delta T$ $R_{th} = 0.1 \text{ K cm}^2 / \text{ W}$ $\dot{q}'' = 50 - 100 \text{ W/cm}^2$ $\Delta T = 5 - 10^{\circ} \text{ C}$

Air

1. Low heat capacity

 $c_{\rm V} \approx ~0.0003~Wh/(L\cdot K)$

2. High thermal resistance



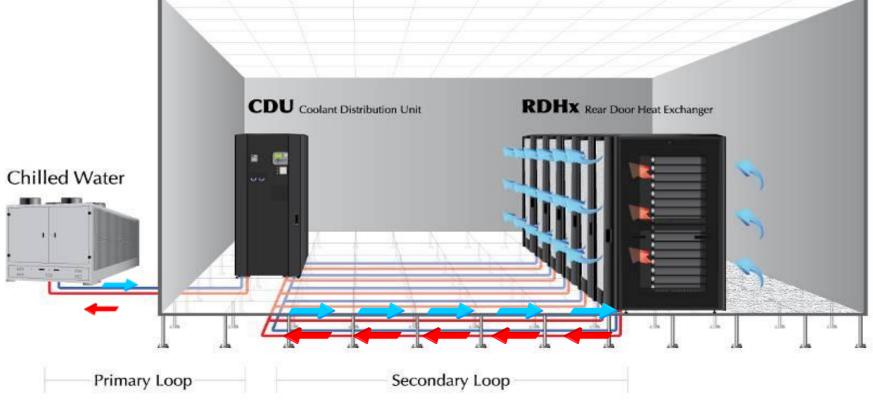
 $\dot{q}'' = R_{\rm th} \cdot \Delta T$

- $R_{\rm th} = 1 \,\,\mathrm{K}\,\mathrm{cm}^2/\,\mathrm{W}$
- $\dot{q}'' = 50 100 \text{ W/cm}^2$
- $\Delta T = 50 100^{\circ} C$

IBM Rear Door Heat Exchangers



- Passive Rear Door Heat Exchanger (RDHx) provides up to 100% cooling
 - No condensation, no need for reheat or humidification
 - No moving parts
- CDU creates fully isolated, temperature controlled secondary loop





Direct Water Cooling

IBM iDataplex dx360 M4

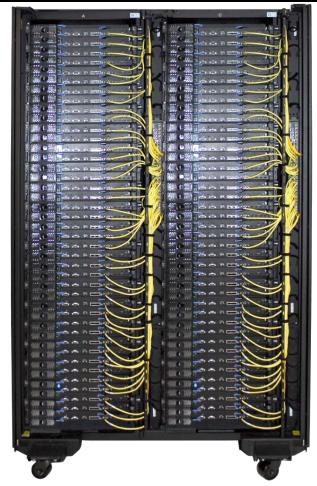




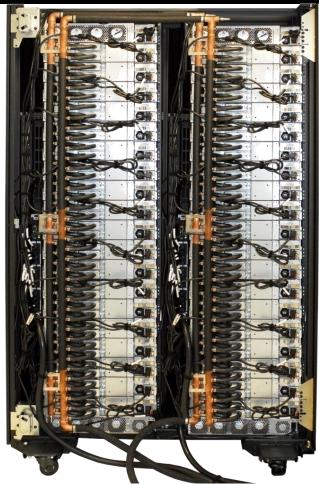
- Heat flux > 90% to water; very low chilled water requirement
- Power advantage over air-cooled node:
 - Warm water cooled ~10% (cold water cooled ~15%)
 - due to lower T_{components} and no fans
- Typical operating conditions: $T_{air} = 25 35^{\circ}C$, $T_{water} = 18 45^{\circ}C$

l_rz-

IBM System x iDataPlex Direct Water Cooled Rack



iDataplex DWC Rack w/ water cooled nodes (front view)



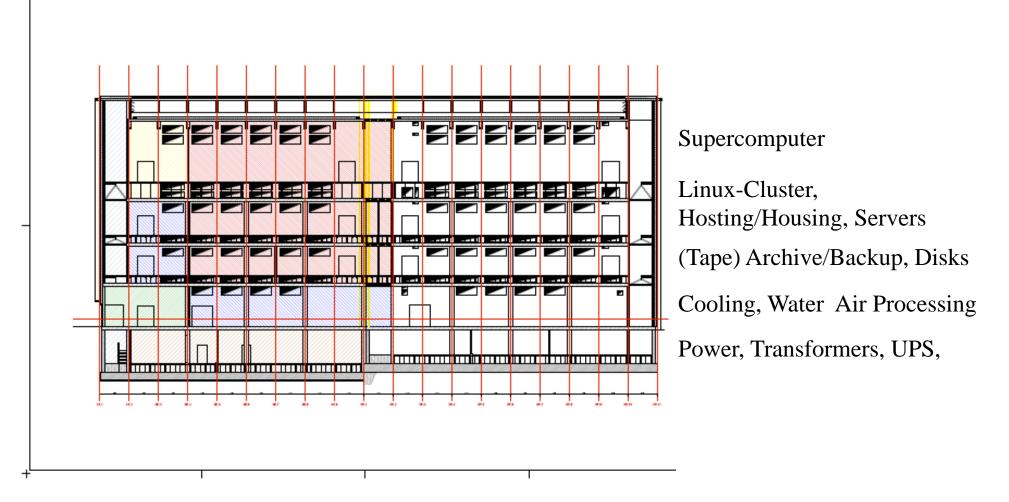
ooled iDataplex DWC Rack w/ water cooled nodes (rear view of water manifolds) Torsten Bloth, IBM Lab Services - © IBM Corporation Hybrid Datacenter w/ Direct Water Cooled Nodes

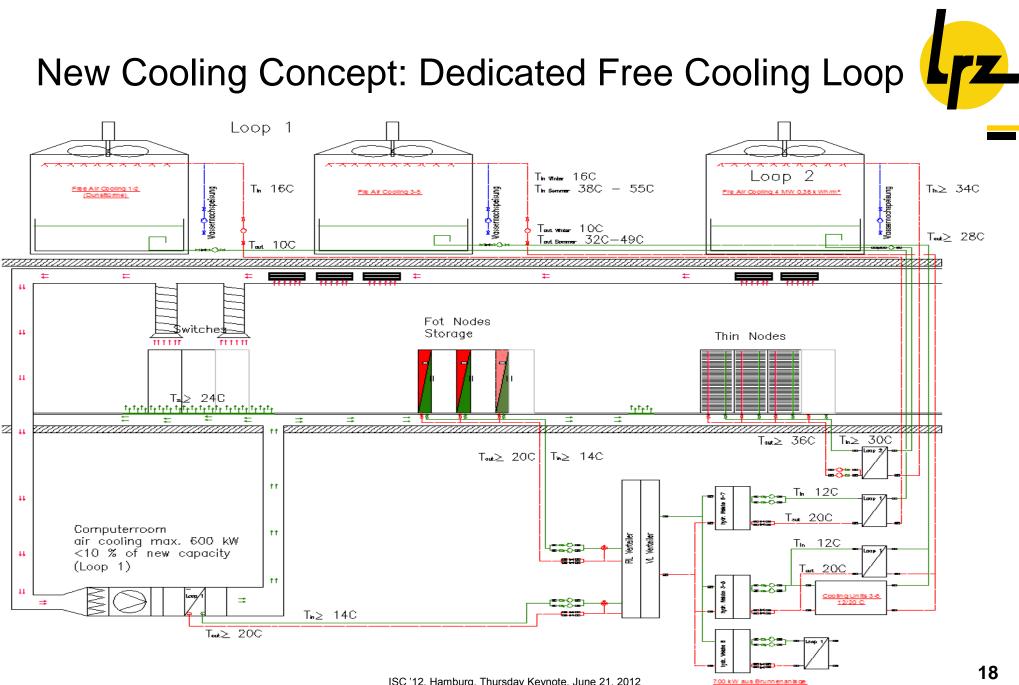
- Highly energy-efficient hybrid-cooling solution:
 - Compute racks
 - 90% Heat flux to warm water
 - 10% Heat flux to CRAH
 - Switch / Storage racks
 - Rear door heat exchangers
- Compute node power consumption reduced ~ 10% due to lower component temperatures and no fans.
- Power Usage Effectiveness P_{Total} / P_{IT}: PUE ~ 1.1



From Computer Cube to Computer Cuboid Nearly double floor space







Infrastructure 3rd floor







Infrastructure 3rd floor







Infrastructure 4th floor







Infrastructure roof





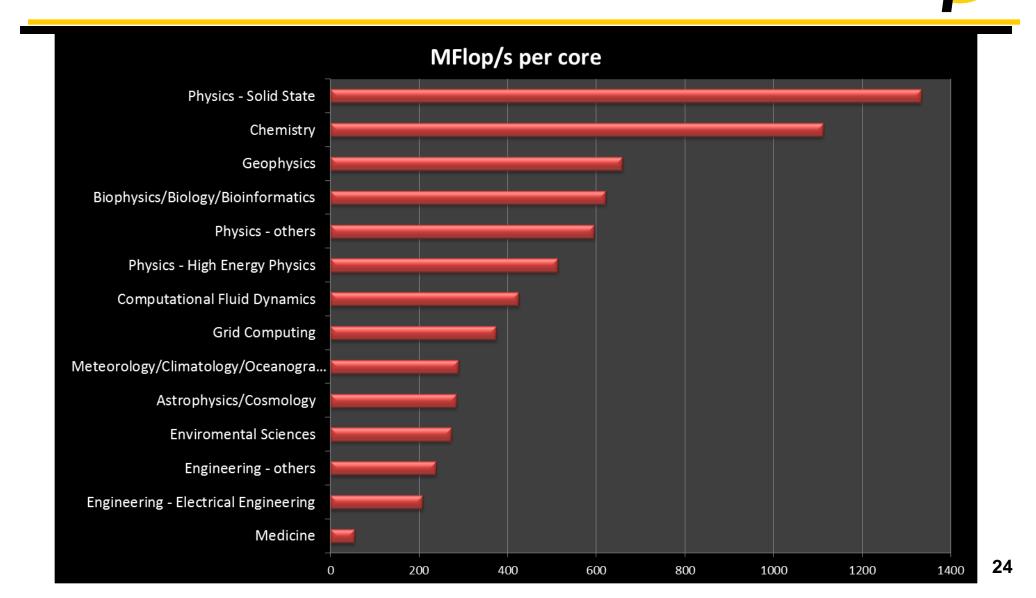




GPGPUs for Dusty Decks?

- □ Manycore for Scientific Applications
 - Statistics at LRZ: Percentage of max performance for different application areas
- Minimizing Energy Maximizes Programming Cost and Minimizes System Performance Yield
- □ We need tools to find a global optimum!

Performance per core by Research area





□ Node Power Consumption and Cooling Infrastructure

- □ Using the characteristics of new processor products (SB EP)
 - Power Efficiency depends on clock speed (parabolic curve)
 - Power Efficiency depends on voltage (lower is better)
 - Turbo mode is good for TOP500 not for Power Efficiency
 - Energy loss with increased chip temperature Cool down as much as possible (cost for cooling?) Use liquid instead of air
 - Disable unnecessary units (virtualization, ...)
 - Need to adjust processor, memory and interconnect speed

Measurement data



SuperMUC HPL Power Consumption (Infrastructure + Machine Room + PDU Measurement) 4000 50000 45000 3500 40000 3000 35000 (HN) 30000 (0000) 25000 20000 20000 15000 Energy Construction (0000) 2500 Power (Machine Room, kW) Power (kW) Power (PDU, kW) Power (infrastructure, kW) 2000 - Energy (Machine Room, kWh) Integrated Power (PDUs, kWh) 1500 HPL Start ••••• HPL End 1000 10000 500 5000 0 0 16:30 18:00 19:30 21:00 22:30 0:00 1:30 3:00 4:30 6:00 7:30 9:00 10:30 12:00 13:30 Time (Clock)

Deutscher Rechenzentrumspreis 2012

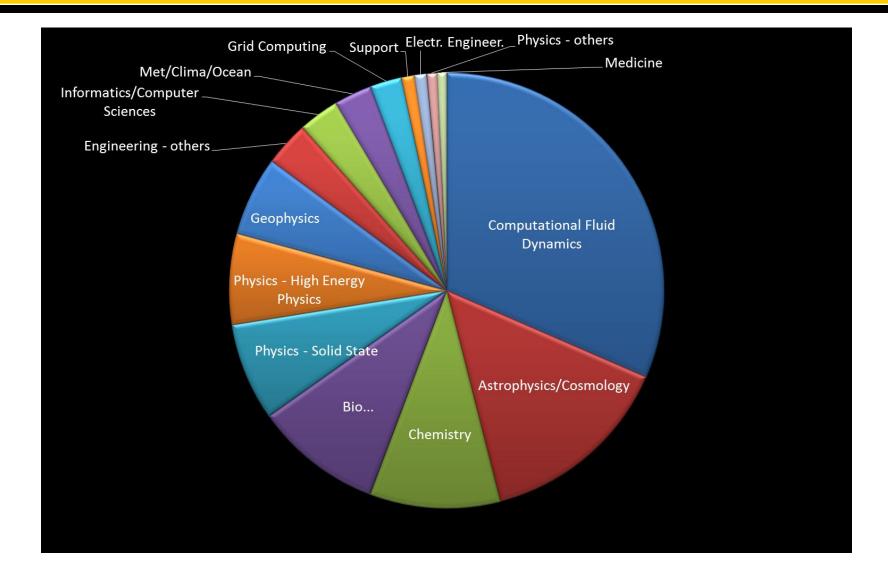
SuperMUC users and network



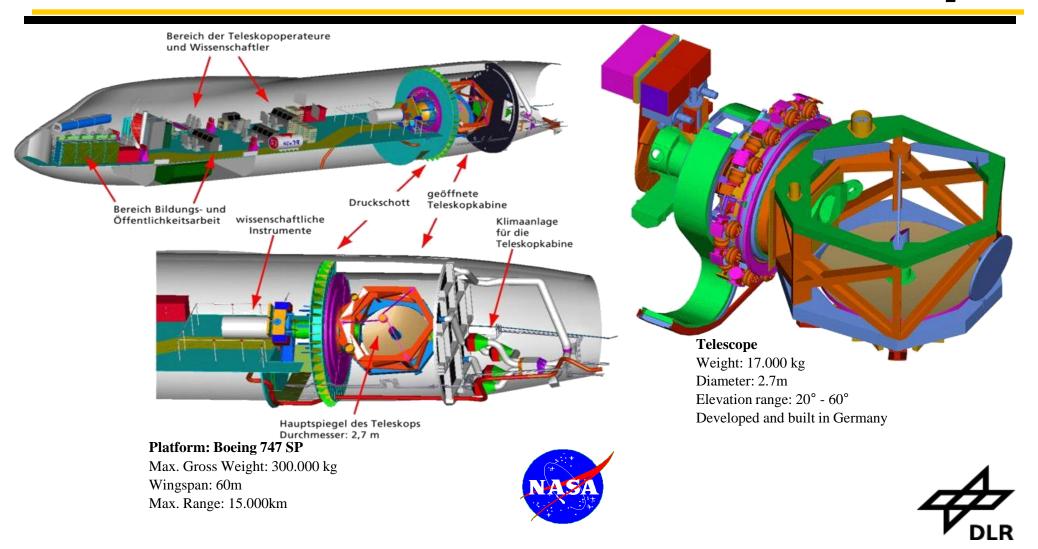


Usage 2010 by Research Area





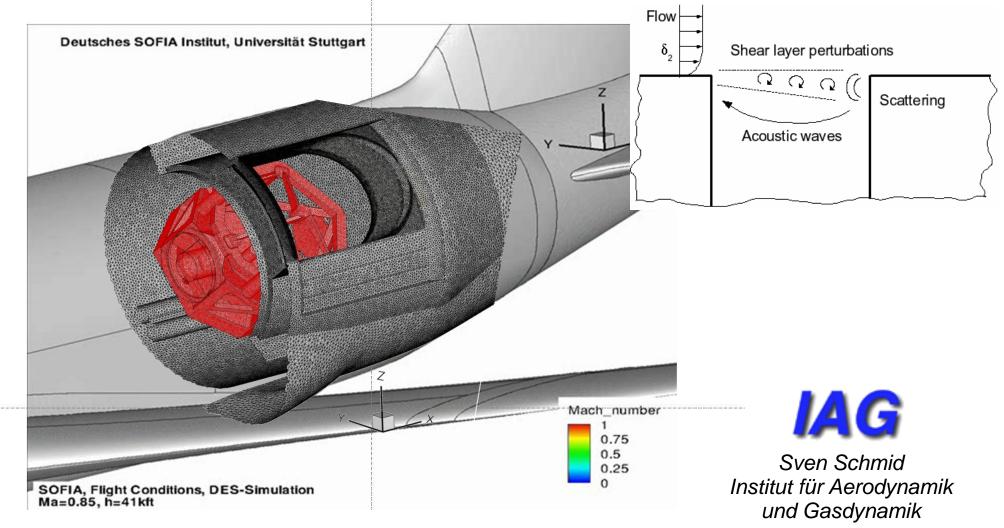
Stratospheric Observatory For Infrared Astronomy



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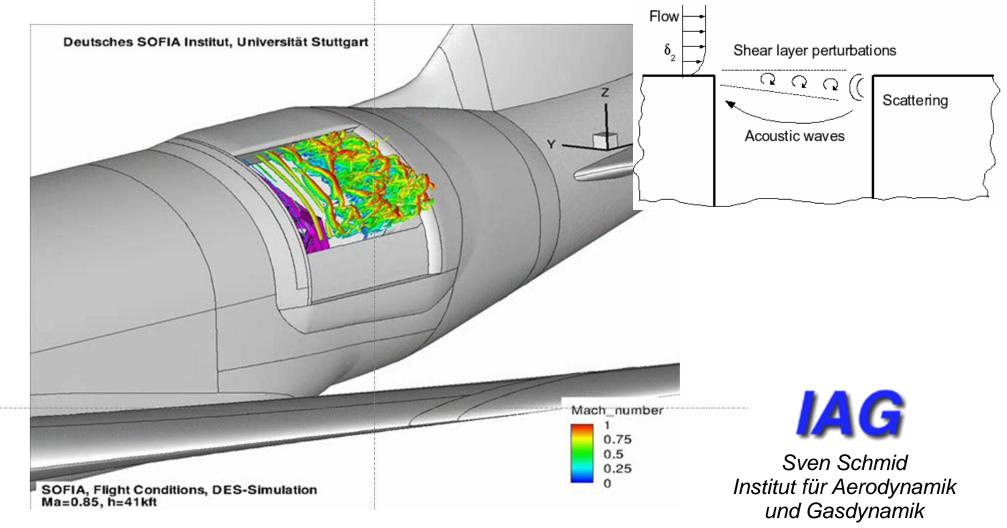
Shear Layer Visualization



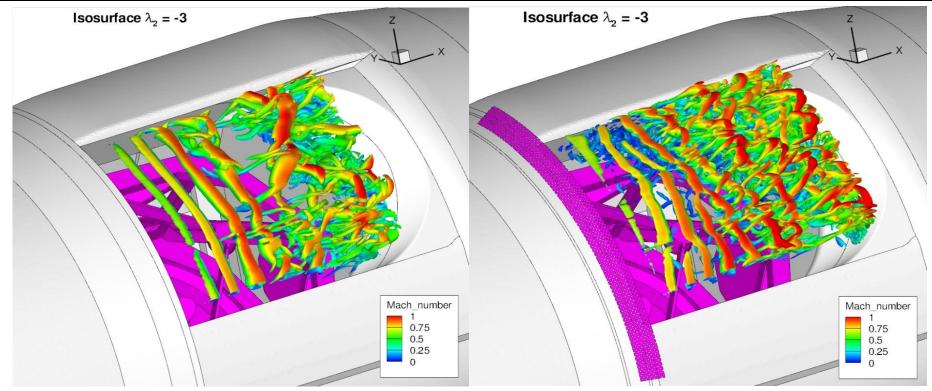


Shear Layer Visualization





SOFIA Configuration with Fence



SOFIA configuration without fence

SOFIA configuration with fence

Numerical investigation of the noise from modified nozzle geometries





Rolls-Royce Trent 1000 engine on a Boing 787 Dreamliner (copyright Rolls-Royce plc 2010)

Institute of Fluid Mechanics and Engineering Acoustics, University of Technology Berlin

Principal Investigator

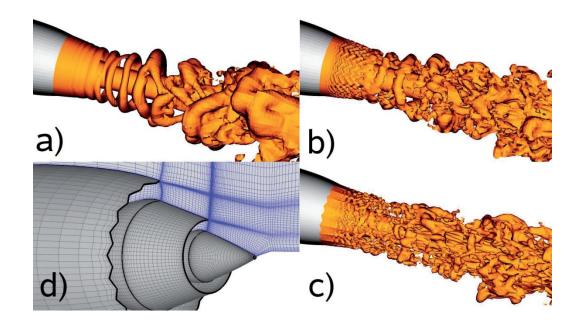
F. Thiele

Researchers

D. Eschricht, J. Yan, L. Panek and K. Tawackolian

Project Partners

Rolls-Royce Germany

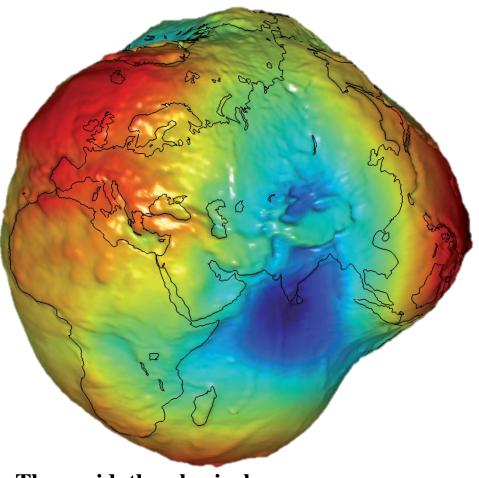


Iso-surfaces of the density showing resolved turbulent flow structures

- a) standard DES;
- b) modified DES, unserrated nozzle;
- c) modified DES, serrated nozzle;
- d) serrated short-cowl nozzle surface mesh with every second grid line shown

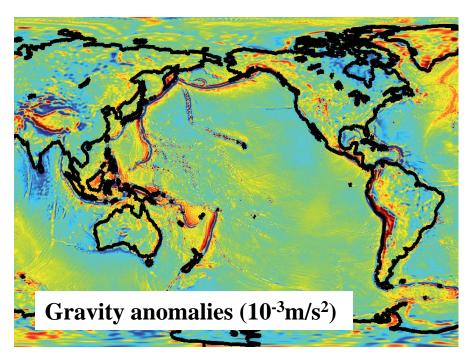
Global high-resolution gravity field determination





The geoid, the physical shape of the Earth

Research Institution Institute for Astronomical and Physical Geodesy Principal Investigator Thomas Gruber Researchers Thomas Fecher, Prof Roland Pail



SuperMUC and after



• SuperMUC Phase 2 in 2014:

manycore technology better energy efficiency

- LRZ in Exascale projects: DEEP (Intel MIC and Xtoll) Mont-Blanc (ARM technology) EESI
- Successor to SuperMUC needs strong support for users: Scalability issues for the "Mega-core-system"
- New "HPC styles":

Big Data Realtime HPC Integrated Visualization Steering